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<u>·</u>	Application No.	Applicant(s)	_AK
Notice of Allowability	10/689,936	OHSUMI, TAKASHI	
	Examiner	Art Unit	
	Fernando L. Toledo	2823	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT F of the Office or upon petition by the applicant. See 37 CFR 1.31	S (OR REMAINS) CLOSED in this ap 5) or other appropriate communicatio RIGHTS. This application is subject t	oplication. If not include n will be mailed in due o	d course. <b>THIS</b>
1. A This communication is responsive to <u>a telephone intervie</u>	w and examiner's amendment of Jur	<u>e 6, 2005</u> .	
2. The allowed claim(s) is/are 27 and 28.	-		
3. The drawings filed on 22 October 2003 are accepted by the	he Examiner.		
<ul> <li>4.  Acknowledgment is made of a claim for foreign priority to a)  All b)  Some* c)  None of the:  1.  Certified copies of the priority documents have 2.  Certified copies of the priority documents have 3.  Copies of the certified copies of the priority documents have 1.  Copies of the certified copies of the priority documents have 2.  Certified copies of the certified copies of the priority documents have 1.  Certified copies of the certified copies of the priority documents have 1.  Certified copies not received:  * Certified copies not received:  Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON! THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.</li> <li>5.  A SUBSTITUTE OATH OR DECLARATION must be subtracted as the priority documents have 2.  Certified copies of the priority documents have 2.  Certified copie</li></ul>	re been received. re been received in Application No. <u>o</u> ocuments have been received in this r of this communication to file a reply MENT of this application. mitted. Note the attached EXAMINER	r national stage applicat r complying with the req	uirements
		ation is deficient.	
6. CORRECTED DRAWINGS (as "replacement sheets") mu  (a) including changes required by the Notice of Draftsper		-948) attached	
1) ☐ hereto or 2) ☐ to Paper No./Mail Date	· ·	,	
(b) ☐ including changes required by the attached Examined Paper No./Mail Date			
Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in			back) of
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT	osit of BIOLOGICAL MATERIAL FOR THE DEPOSIT OF BIOLOGIC	must be submitted. N CAL MATERIAL.	ote the
Attachment(s)  1. Notice of References Cited (PTO-892)	5. ☐ Notice of Informal I	Patent Application (PTC	)-152)

Paper No./Mail Date

of Biological Material

2. Notice of Draftperson's Patent Drawing Review (PTO-948)

4. 

Examiner's Comment Regarding Requirement for Deposit

3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),

6. ☑ Interview Summary (PTO-413),
Paper No./Mail Date <u>06/06/2005</u>.
7. ☑ Examiner's Amendment/Comment

9. Other \_\_\_\_\_.

8. 

Examiner's Statement of Reasons for Allowance

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## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Andrew Telez on 6 June 2005.

The application has been amended as follows:

In the claims:

27 (amended) A method for fabricating a semiconductor apparatus comprising:

forming a semiconductor integrated circuit on a first surface of a semiconductor substrate;

forming a base member of insulating material on the first surface of the semiconductor substrate;

forming a conductive layer directly on the first surface of the semiconductor substrate, the conductive layer being connected to the semiconductor integrated circuit and having an extended portion that extends onto a top surface of the base member;

placing the first surface of the semiconductor substrate having the semiconductor integrated circuit, the base member and the conductive layer thereon as facing a connection substrate;

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connecting the extended portion of the conductive layer to the connection substrate; and

supplying a sealing member in a space between the two semiconductor substrate and the connection substrate, after said connecting,

the base member and the sealing member having a same thermal expansion coefficient,

the conductive layer is formed so that the extended portion extends onto an entirety of the top surface of the base member.

## Claim 31 (Canceled)

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fernando L. Toledo whose telephone number is 571-272-1867. The examiner can normally be reached on Mon-Thu 7am to 5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on 571-272-1855. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

> George Fourson **Primary Examiner** Art Unit 2823

FToledo

6 June 2005